<b>4</b> )	Туре	L#	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	10838	dll	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/06/01 15:12
2	BRS	L2	1377724	filter	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/06/01 15:12
3	BRS	L3	19533	charge adj2 pump	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/06/01 15:12
4	BRS	L4	45762	delay adj1 line	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/06/01 15:12
5	BRS	L5	1417418	2 or 3 or 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/06/01 15:13
6	BRS	L6	94	2 with 3 with 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/06/01 15:13
7	BRS	L7	563115	resistor\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/06/01 15:13
8	BRS	L8	623732	capacitor\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/06/01 15:13

-	Туре	, L#	Hits	Search Text	DBs	Time Stamp
9	BRS	L9	3	6 with 7 with 8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/06/01 15:14
10	BRS	L10	3	6 same 7 same 8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/06/01 15:14
11	BRS .	L11	707271	6 and 7a nd 8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/06/01 15:14
12	BRS	L12	19	6 and 7 and 8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/06/01 15:29
13	BRS	L13	16	chip and 12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/06/01 15:37
14	BRS	L14	0	chip and 12first adj1 3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/06/01 15:37
15	BRS	L15	1296	first adj3 3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/06/01 15:37
16	BRS	L16	1302	second adj3 3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/06/01 15:37

	Туре	L#	Hits	Search Text	DBs	Time Stamp
17	BRS	L17	403	7 and 8 and 15 and 16	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/06/01 15:37